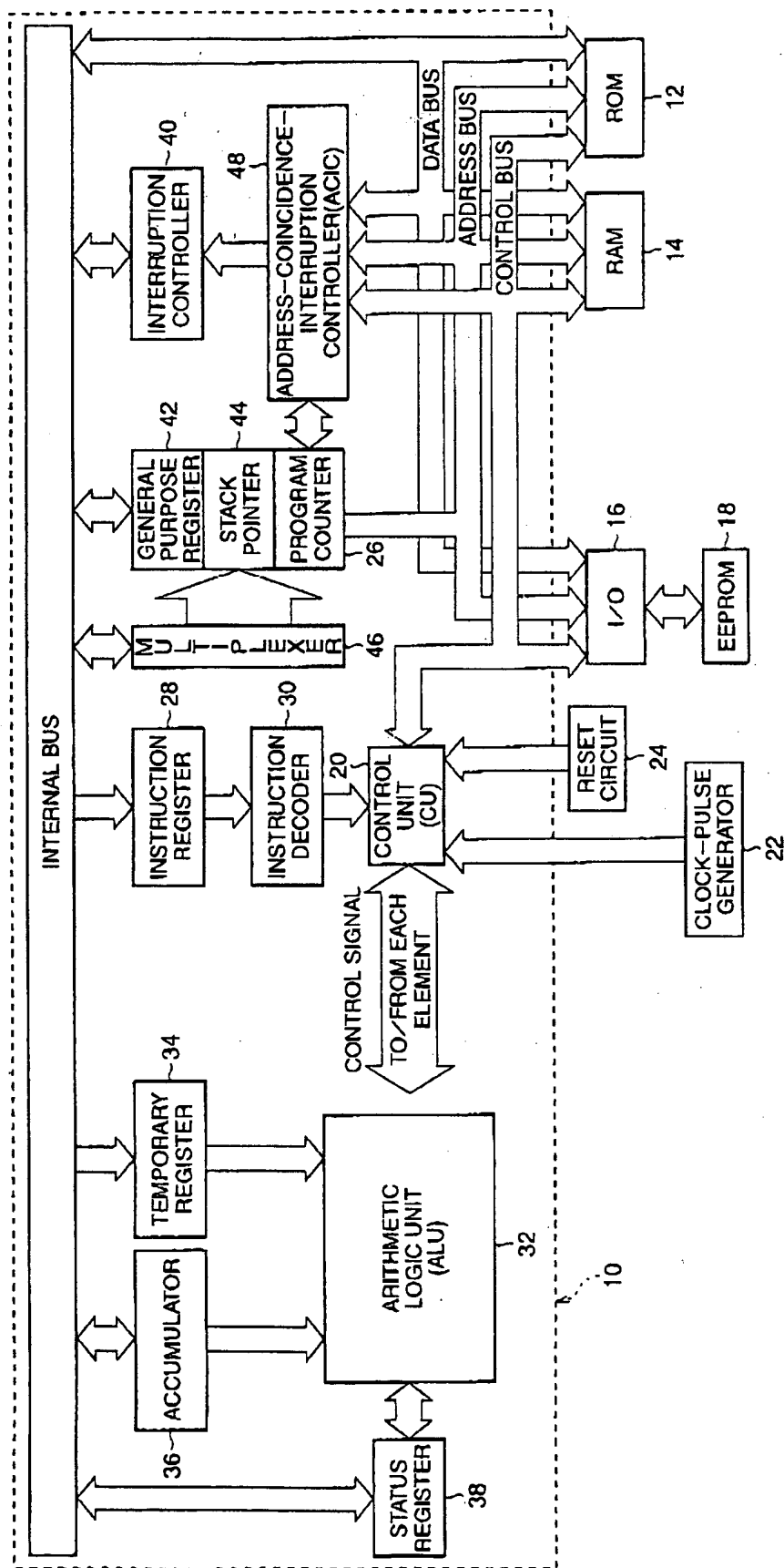


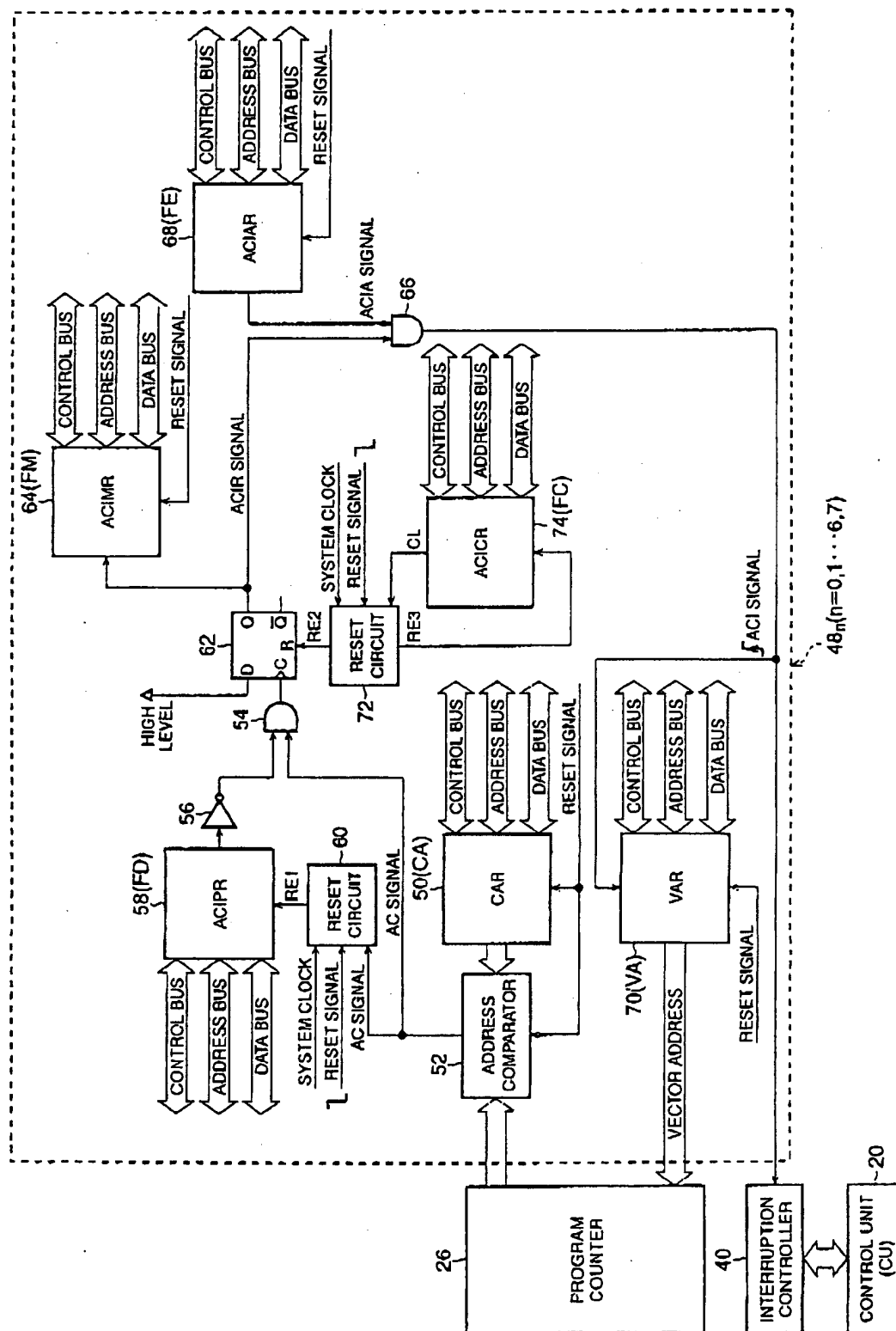
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FIG. 1



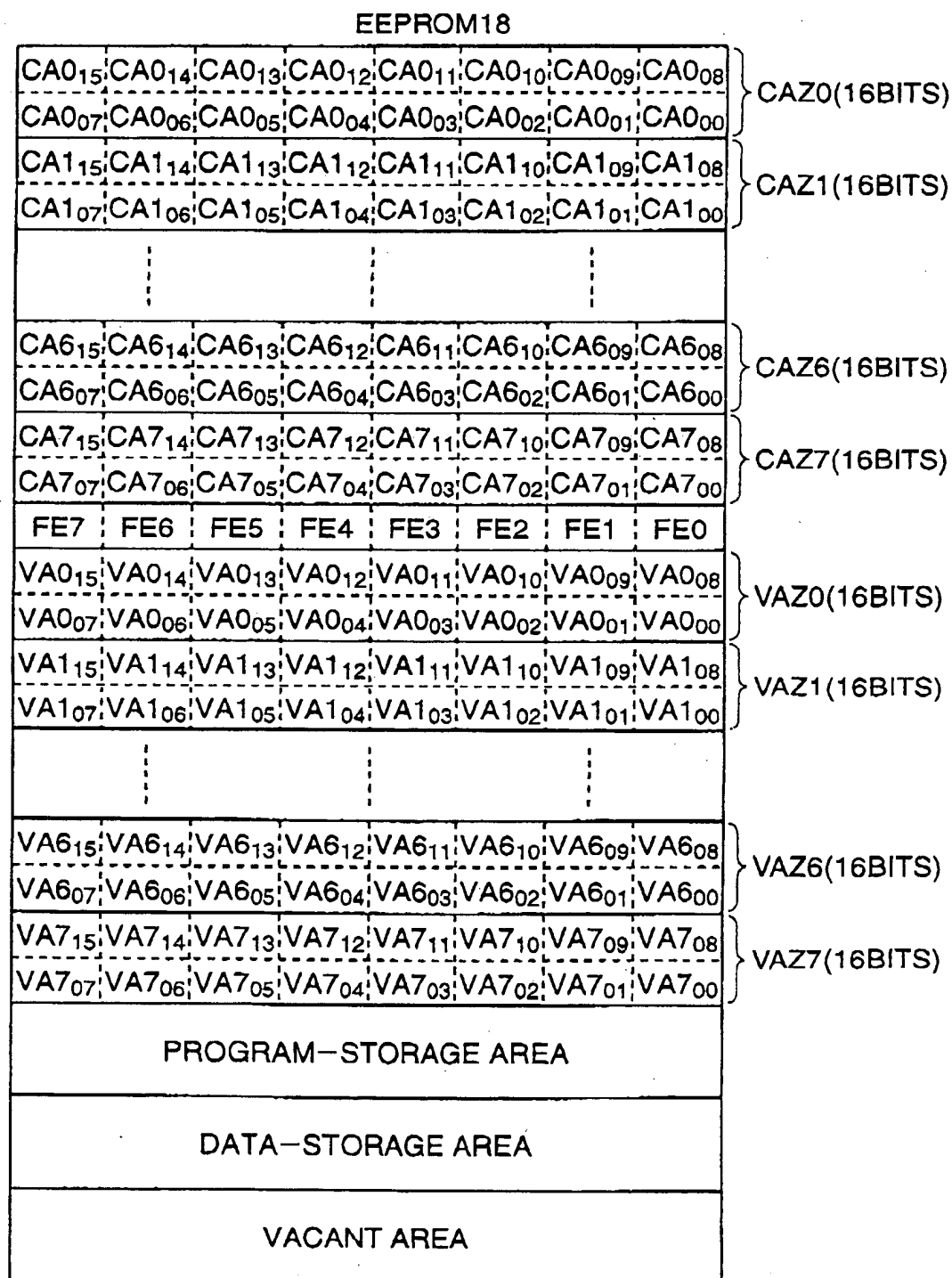
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FIG. 2



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FIG. 3



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FIG. 4

CIRCUIT ELEMENT 48₀

CAR 50

CA0 ₁₅	CA0 ₁₄	CA0 ₁₃	CA0 ₁₂	CA0 ₁₁	CA0 ₁₀	CA0 ₀₉	CA0 ₀₈
CA0 ₀₇	CA0 ₀₆	CA0 ₀₅	CA0 ₀₄	CA0 ₀₃	CA0 ₀₂	CA0 ₀₁	CA0 ₀₀

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FE0

VAR 70

VA0 ₁₅	VA0 ₁₄	VA0 ₁₃	VA0 ₁₂	VA0 ₁₁	VA0 ₁₀	VA0 ₀₉	VA0 ₀₈
VA0 ₀₇	VA0 ₀₆	VA0 ₀₅	VA0 ₀₄	VA0 ₀₃	VA0 ₀₂	VA0 ₀₁	VA0 ₀₀

FIG. 5

CIRCUIT ELEMENT 48₁

CAR 50

CA1 ₁₅	CA1 ₁₄	CA1 ₁₃	CA1 ₁₂	CA1 ₁₁	CA1 ₁₀	CA1 ₀₉	CA1 ₀₈
CA1 ₀₇	CA1 ₀₆	CA1 ₀₅	CA1 ₀₄	CA1 ₀₃	CA1 ₀₂	CA1 ₀₁	CA1 ₀₀

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FE1

VAR 70

VA1 ₁₅	VA1 ₁₄	VA1 ₁₃	VA1 ₁₂	VA1 ₁₁	VA1 ₁₀	VA1 ₀₉	VA1 ₀₈
VA1 ₀₇	VA1 ₀₆	VA1 ₀₅	VA1 ₀₄	VA1 ₀₃	VA1 ₀₂	VA1 ₀₁	VA1 ₀₀

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FIG. 6

CIRCUIT ELEMENT 48₂

CAR 50

CA2 ₁₅	CA2 ₁₄	CA2 ₁₃	CA2 ₁₂	CA2 ₁₁	CA2 ₁₀	CA2 ₀₉	CA2 ₀₈
CA2 ₀₇	CA2 ₀₆	CA2 ₀₅	CA2 ₀₄	CA2 ₀₃	CA2 ₀₂	CA2 ₀₁	CA2 ₀₀

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FE2

VAR 70

VA2 ₁₅	VA2 ₁₄	VA2 ₁₃	VA2 ₁₂	VA2 ₁₁	VA2 ₁₀	VA2 ₀₉	VA2 ₀₈
VA2 ₀₇	VA2 ₀₆	VA2 ₀₅	VA2 ₀₄	VA2 ₀₃	VA2 ₀₂	VA2 ₀₁	VA2 ₀₀

FIG. 7

CIRCUIT ELEMENT 48₃

CAR 50

CA3 ₁₅	CA3 ₁₄	CA3 ₁₃	CA3 ₁₂	CA3 ₁₁	CA3 ₁₀	CA3 ₀₉	CA3 ₀₈
CA3 ₀₇	CA3 ₀₆	CA3 ₀₅	CA3 ₀₄	CA3 ₀₃	CA3 ₀₂	CA3 ₀₁	CA3 ₀₀

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FE3

VAR 70

VA3 ₁₅	VA3 ₁₄	VA3 ₁₃	VA3 ₁₂	VA3 ₁₁	VA3 ₁₀	VA3 ₀₉	VA3 ₀₈
VA3 ₀₇	VA3 ₀₆	VA3 ₀₅	VA3 ₀₄	VA3 ₀₃	VA3 ₀₂	VA3 ₀₁	VA3 ₀₀

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FIG. 8

CIRCUIT ELEMENT 48₄

CAR 50

CA4 ₁₅	CA4 ₁₄	CA4 ₁₃	CA4 ₁₂	CA4 ₁₁	CA4 ₁₀	CA4 ₀₉	CA4 ₀₈
CA4 ₀₇	CA4 ₀₆	CA4 ₀₅	CA4 ₀₄	CA4 ₀₃	CA4 ₀₂	CA4 ₀₁	CA4 ₀₀

ACIAR 68

FE4

VAR 70

VA4 ₁₅	VA4 ₁₄	VA4 ₁₃	VA4 ₁₂	VA4 ₁₁	VA4 ₁₀	VA4 ₀₉	VA4 ₀₈
VA4 ₀₇	VA4 ₀₆	VA4 ₀₅	VA4 ₀₄	VA4 ₀₃	VA4 ₀₂	VA4 ₀₁	VA4 ₀₀

FIG. 9

CIRCUIT ELEMENT 48₅

CAR 50

CA5 ₁₅	CA5 ₁₄	CA5 ₁₃	CA5 ₁₂	CA5 ₁₁	CA5 ₁₀	CA5 ₀₉	CA5 ₀₈
CA5 ₀₇	CA5 ₀₆	CA5 ₀₅	CA5 ₀₄	CA5 ₀₃	CA5 ₀₂	CA5 ₀₁	CA5 ₀₀

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FE5

VAR 70

VA5 ₁₅	VA5 ₁₄	VA5 ₁₃	VA5 ₁₂	VA5 ₁₁	VA5 ₁₀	VA5 ₀₉	VA5 ₀₈
VA5 ₀₇	VA5 ₀₆	VA5 ₀₅	VA5 ₀₄	VA5 ₀₃	VA5 ₀₂	VA5 ₀₁	VA5 ₀₀

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FIG. 10

CIRCUIT ELEMENT 48₆

CAR 50

CA6 ₁₅	CA6 ₁₄	CA6 ₁₃	CA6 ₁₂	CA6 ₁₁	CA6 ₁₀	CA6 ₀₉	CA6 ₀₈
CA6 ₀₇	CA6 ₀₆	CA6 ₀₅	CA6 ₀₄	CA6 ₀₃	CA6 ₀₂	CA6 ₀₁	CA6 ₀₀

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FE6

VAR 70

VA6 ₁₅	VA6 ₁₄	VA6 ₁₃	VA6 ₁₂	VA6 ₁₁	VA6 ₁₀	VA6 ₀₉	VA6 ₀₈
VA6 ₀₇	VA6 ₀₆	VA6 ₀₅	VA6 ₀₄	VA6 ₀₃	VA6 ₀₂	VA6 ₀₁	VA6 ₀₀

FIG. 11

CIRCUIT ELEMENT 48₇

CAR 50

CA7 ₁₅	CA7 ₁₄	CA7 ₁₃	CA7 ₁₂	CA7 ₁₁	CA7 ₁₀	CA7 ₀₉	CA7 ₀₈
CA7 ₀₇	CA7 ₀₆	CA7 ₀₅	CA7 ₀₄	CA7 ₀₃	CA7 ₀₂	CA7 ₀₁	CA7 ₀₀

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FE7

VAR 70

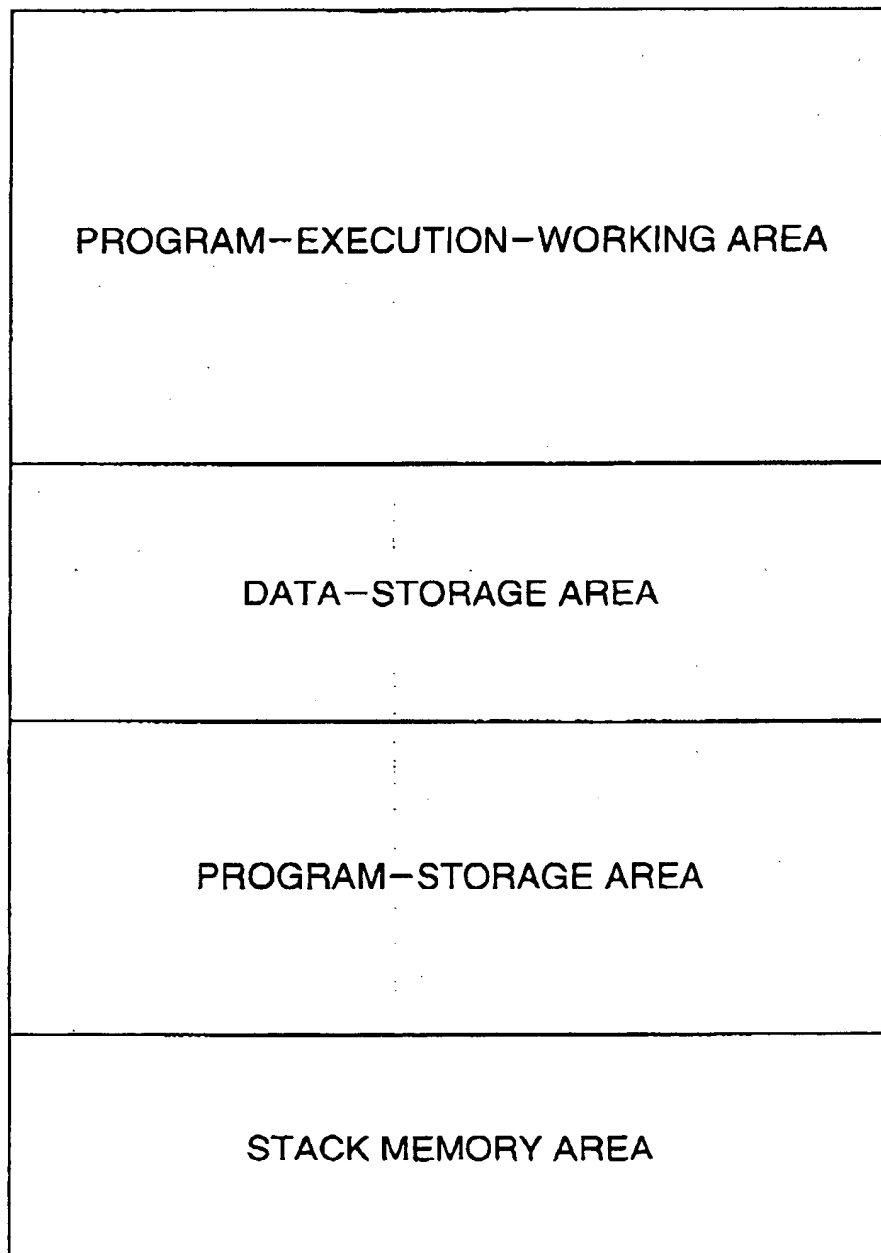
VA7 ₁₅	VA7 ₁₄	VA7 ₁₃	VA7 ₁₂	VA7 ₁₁	VA7 ₁₀	VA7 ₀₉	VA7 ₀₈
VA7 ₀₇	VA7 ₀₆	VA7 ₀₅	VA7 ₀₄	VA7 ₀₃	VA7 ₀₂	VA7 ₀₁	VA7 ₀₀

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FIG. 12

RAM14



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FIG. 13

CIRCUIT ELEMENT 48₀

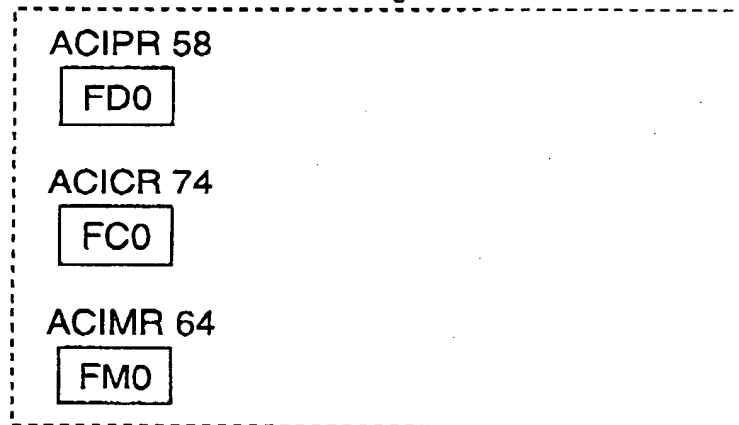
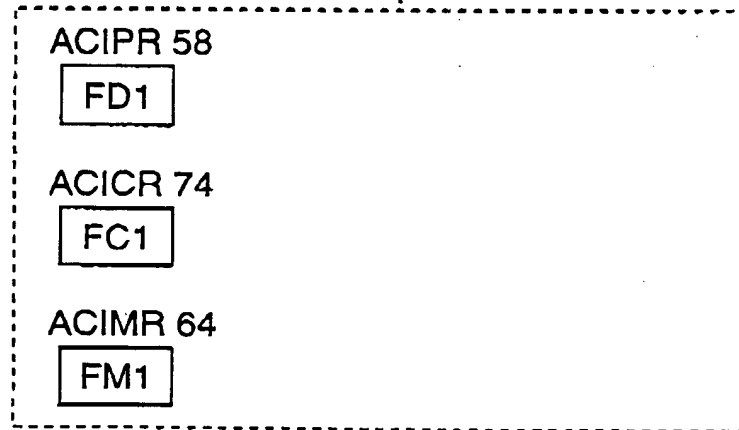


FIG. 14

CIRCUIT ELEMENT 48₁



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FIG. 15

CIRCUIT ELEMENT 48₂

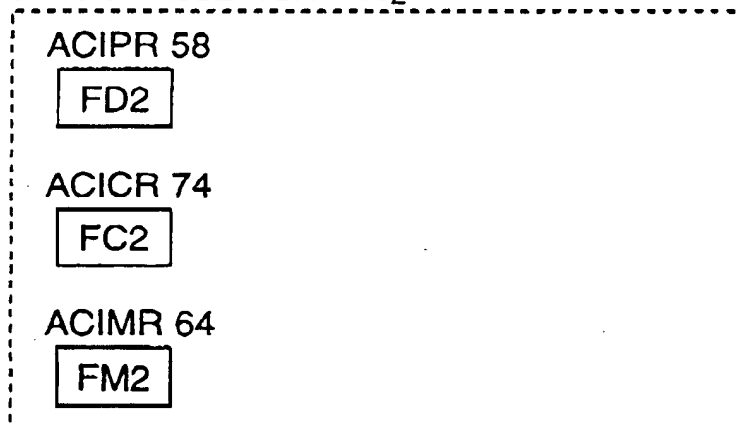
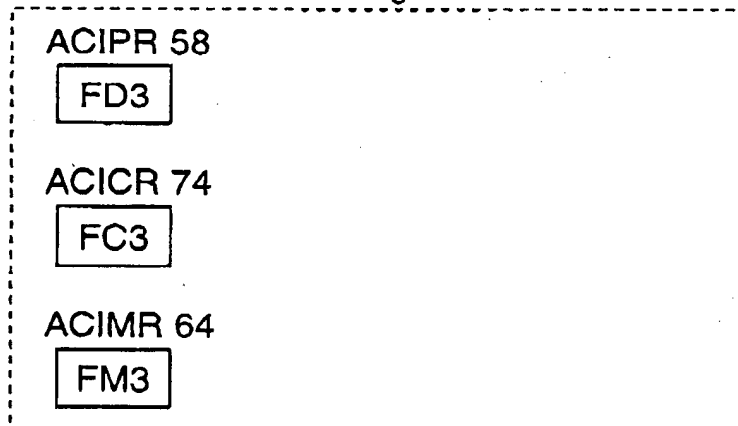


FIG. 16

CIRCUIT ELEMENT 48₃



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FIG. 17

CIRCUIT ELEMENT 48₄

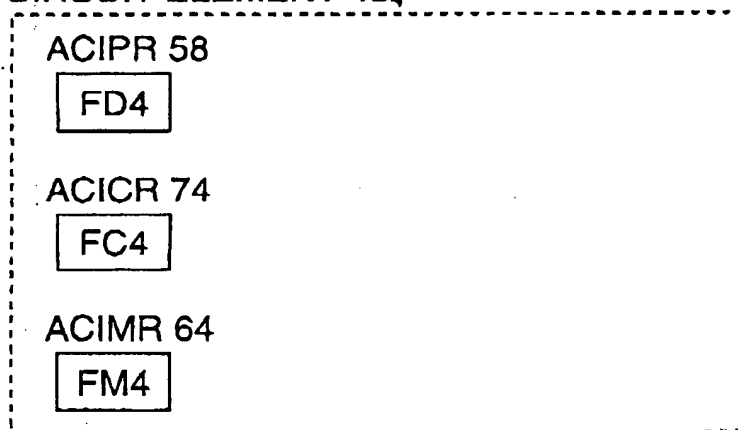
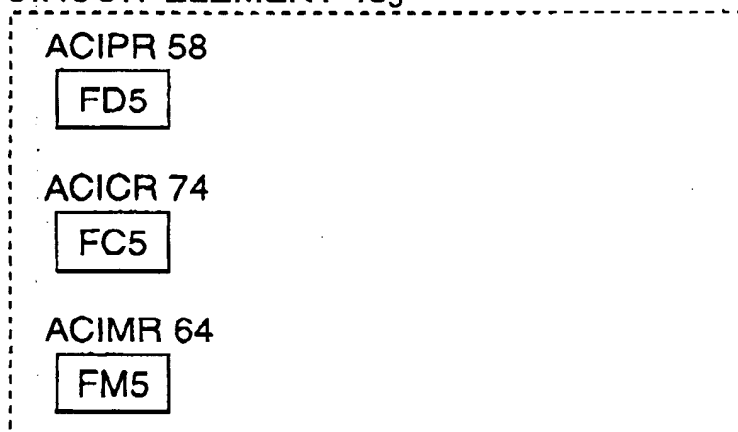


FIG. 18

CIRCUIT ELEMENT 48₅



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FIG. 19

CIRCUIT ELEMENT 48₆

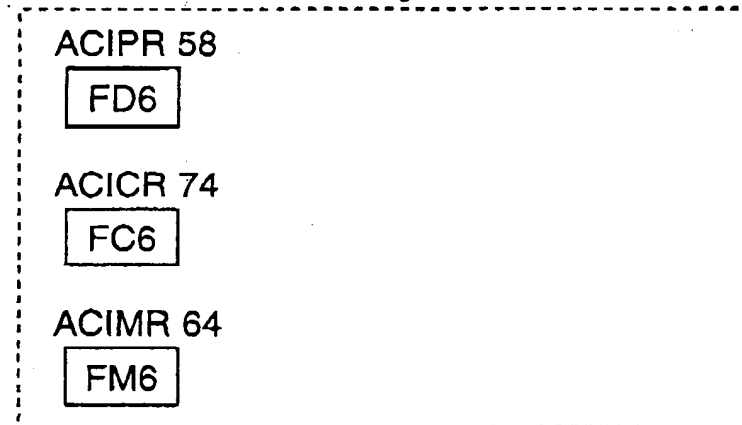
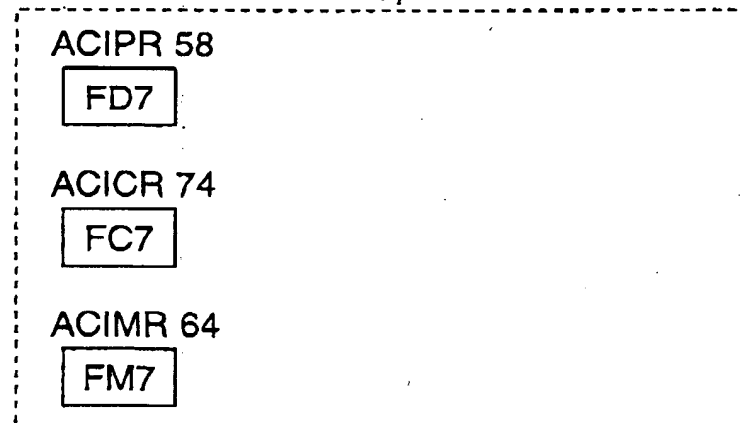


FIG. 20

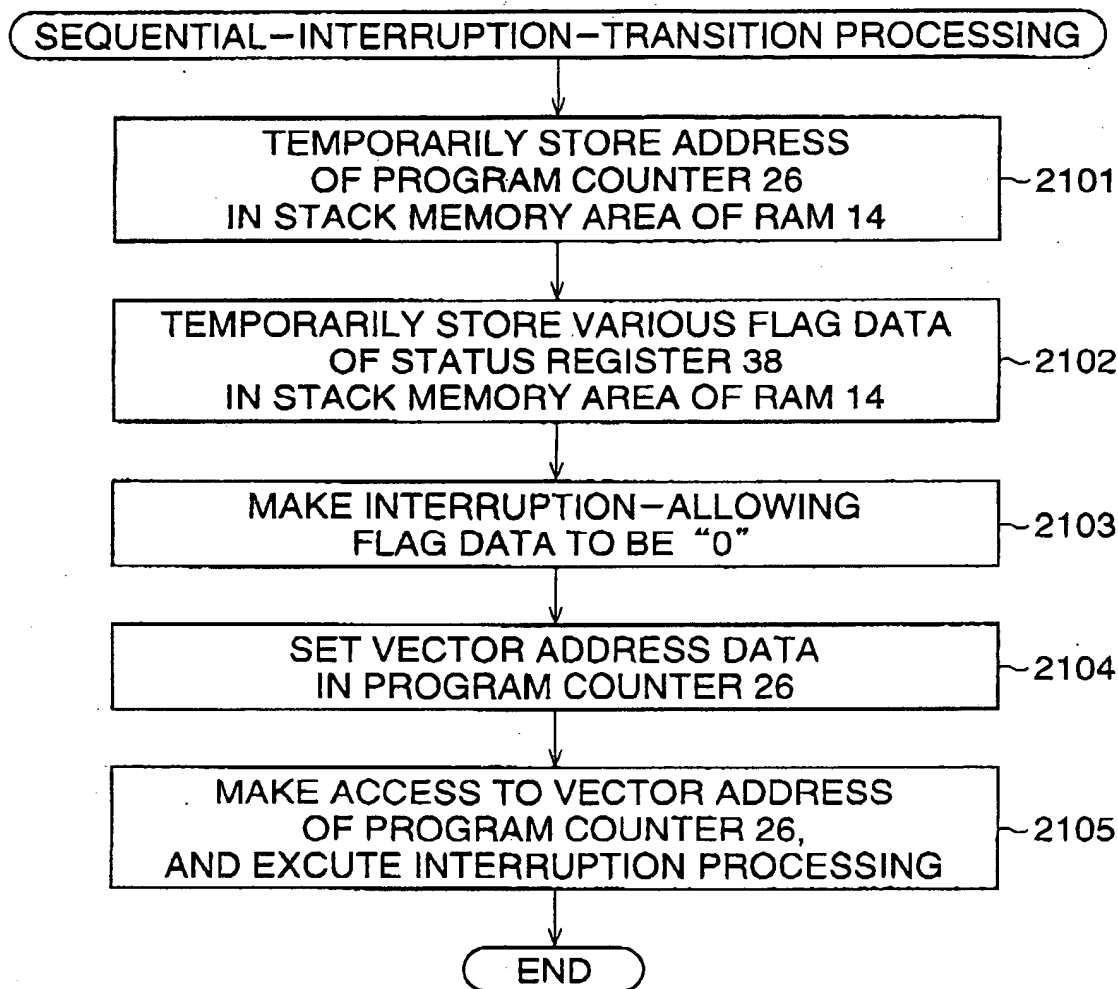
CIRCUIT ELEMENT 48₇



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FIG. 21



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FIG. 22

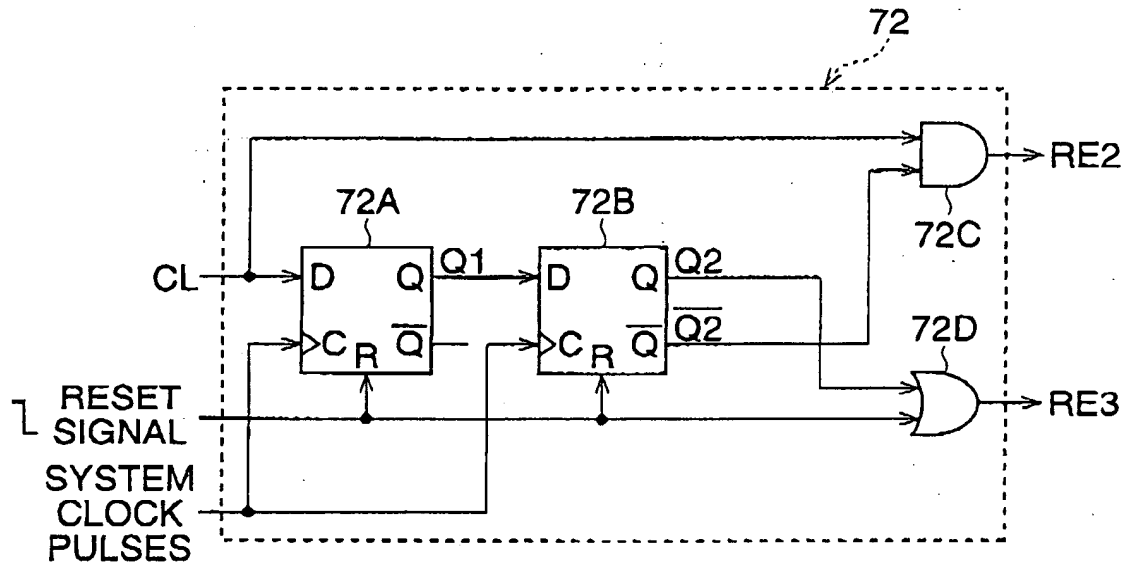
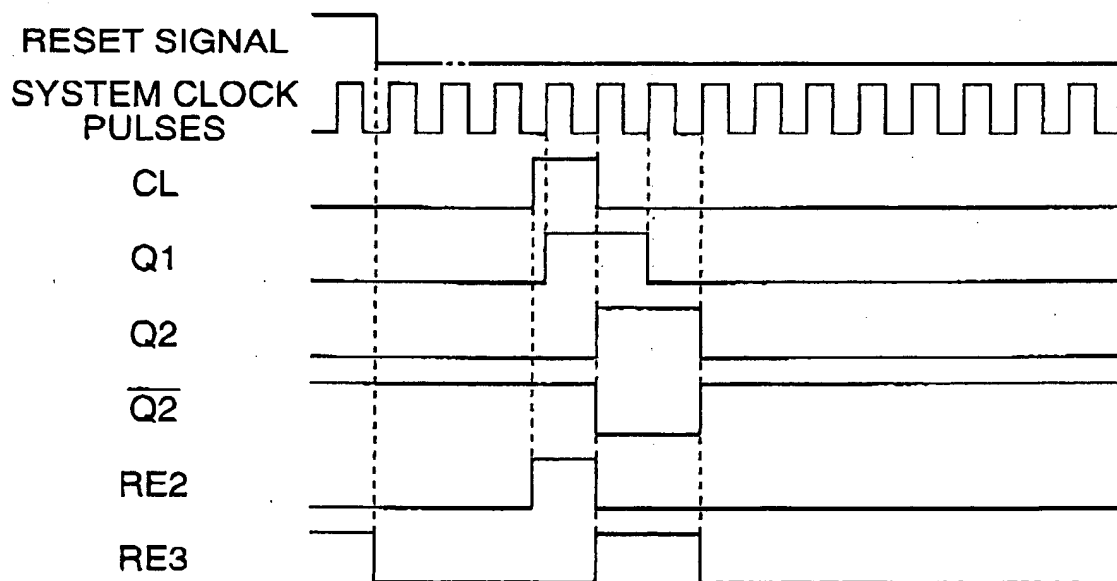


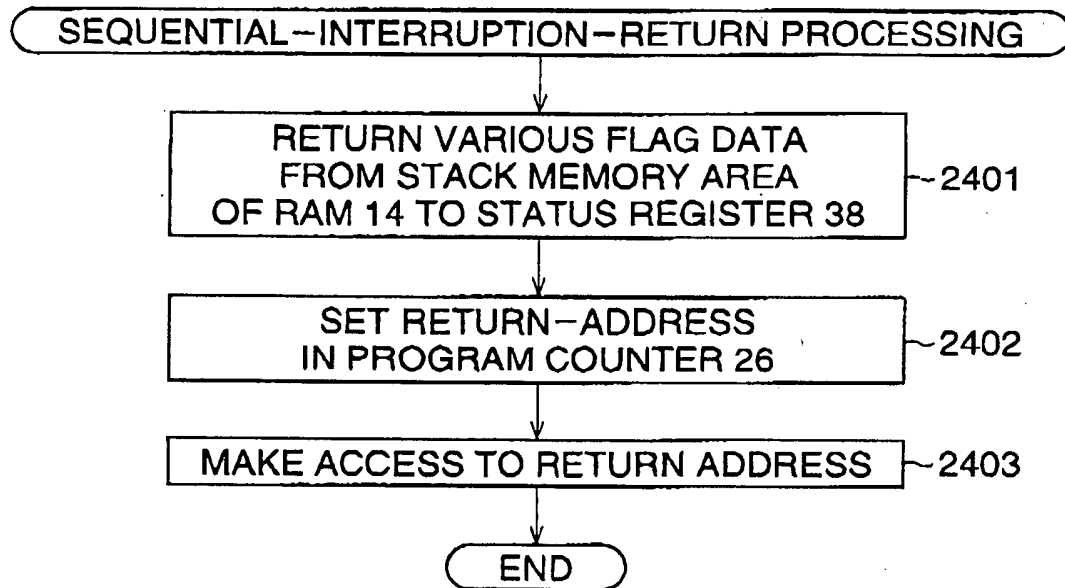
FIG. 23



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FIG. 24



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FIG. 25

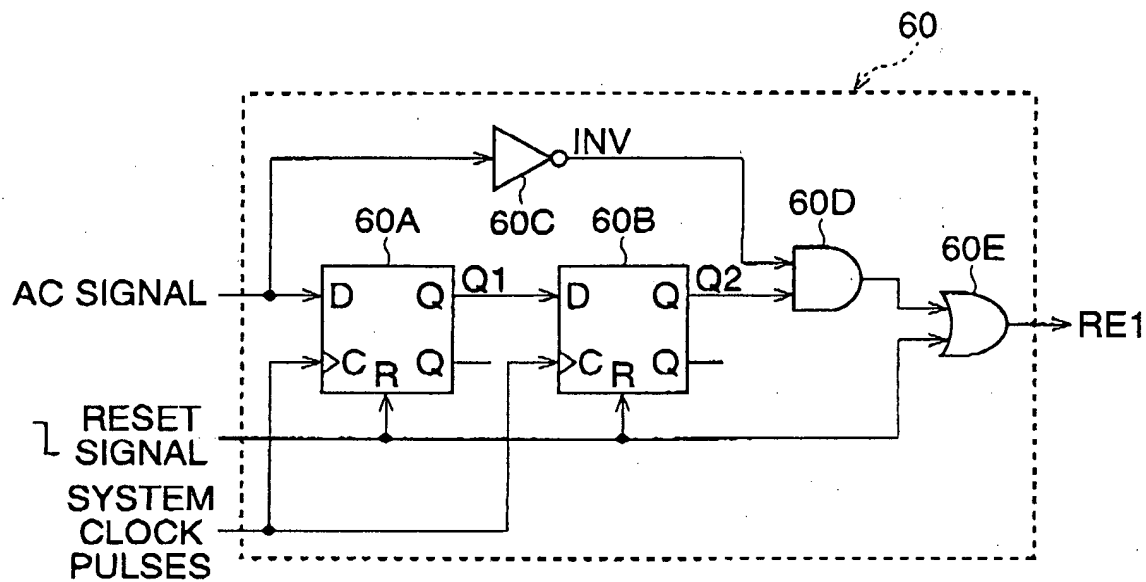
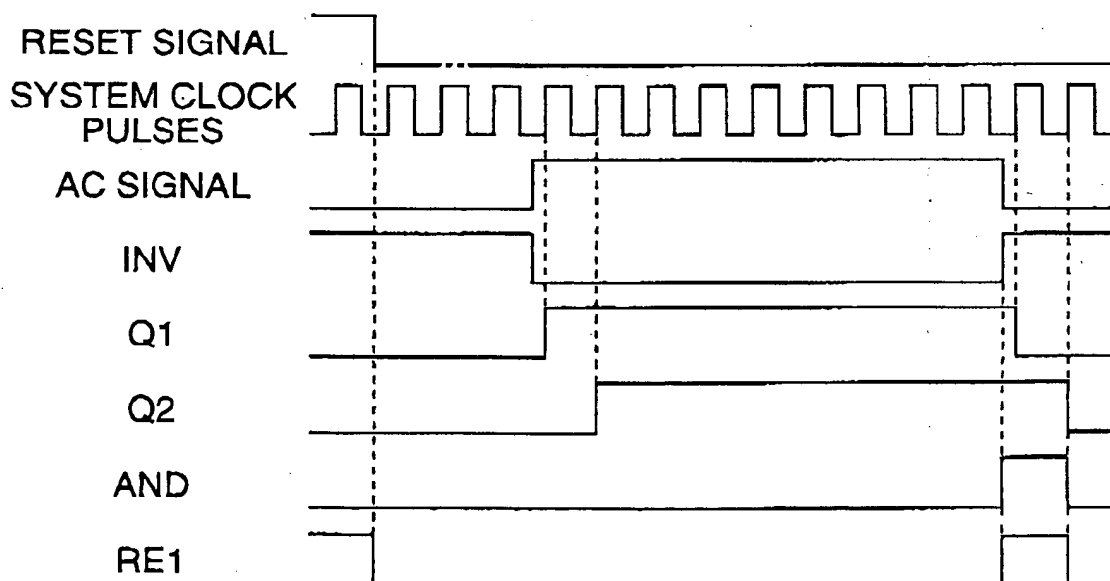
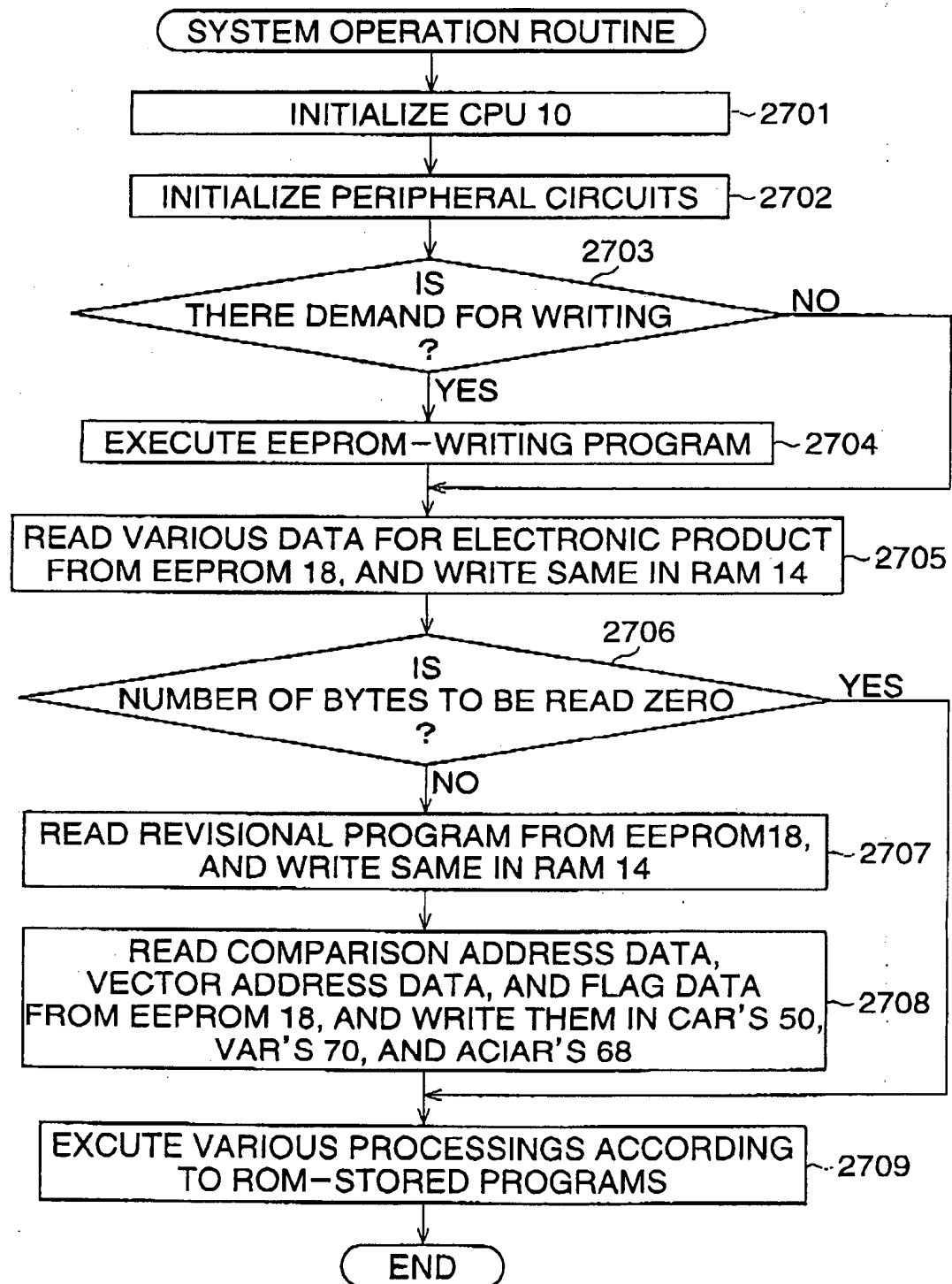


FIG. 26



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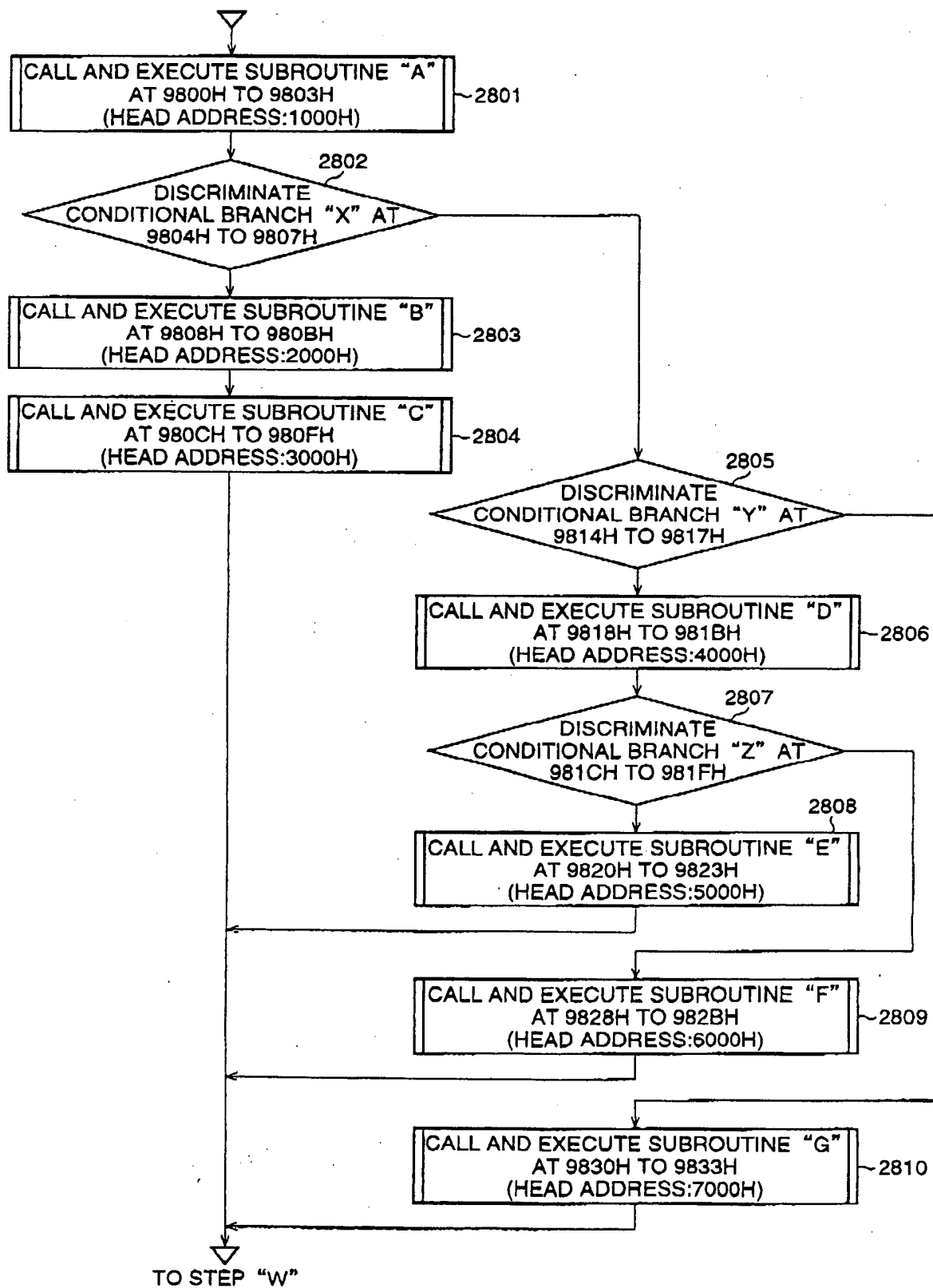
FIG. 27



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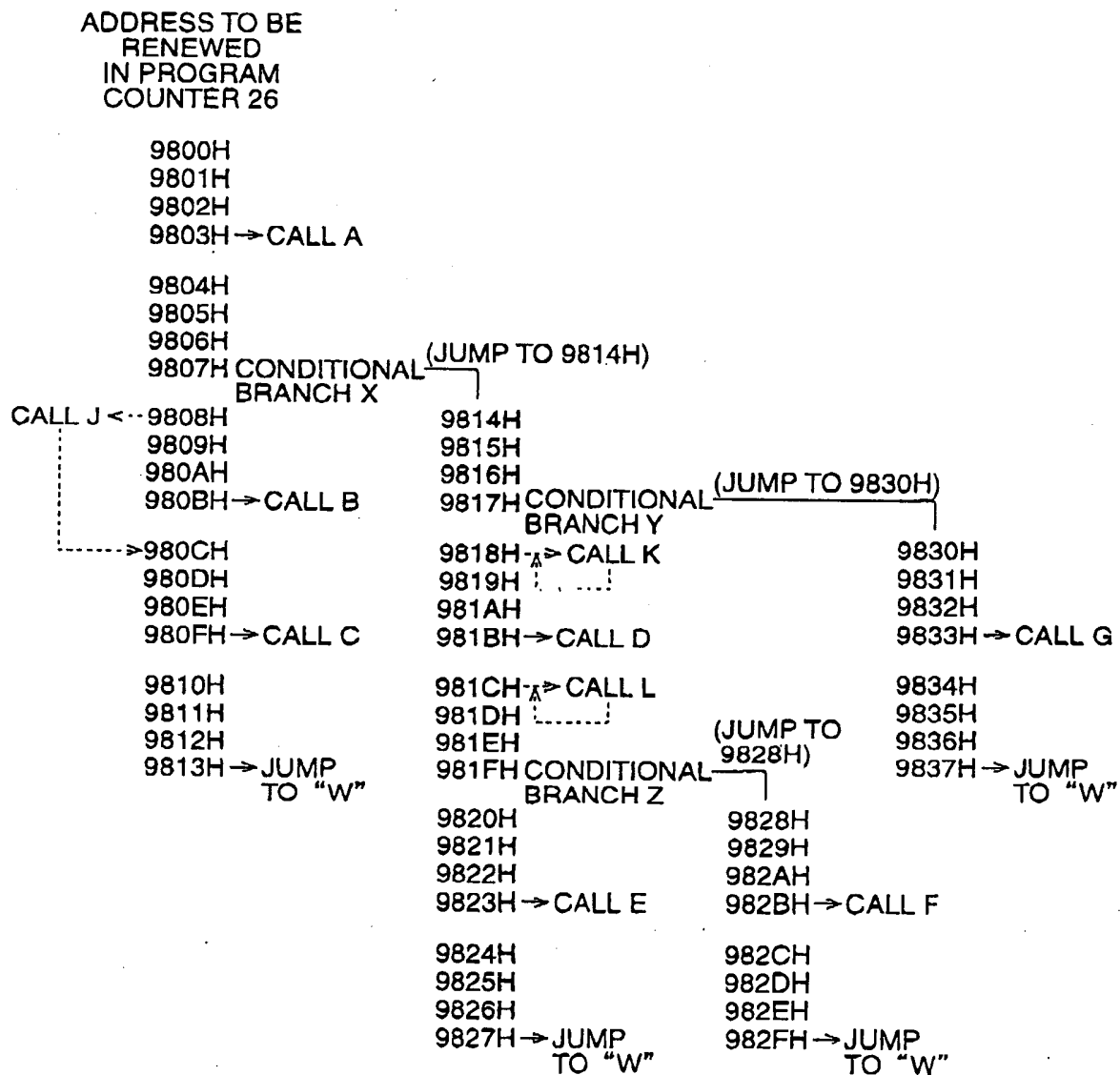
FIG. 28



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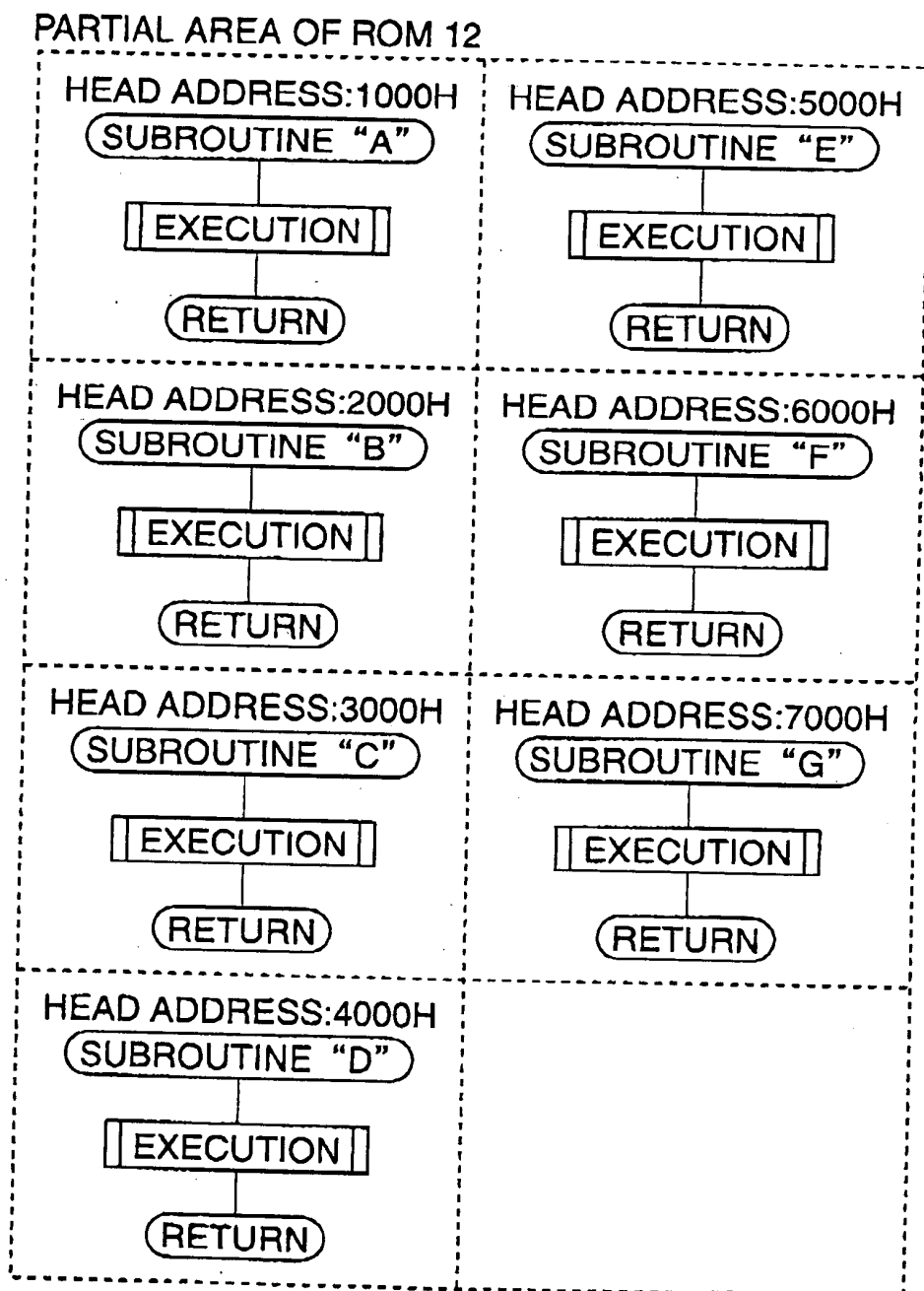
FIG. 29



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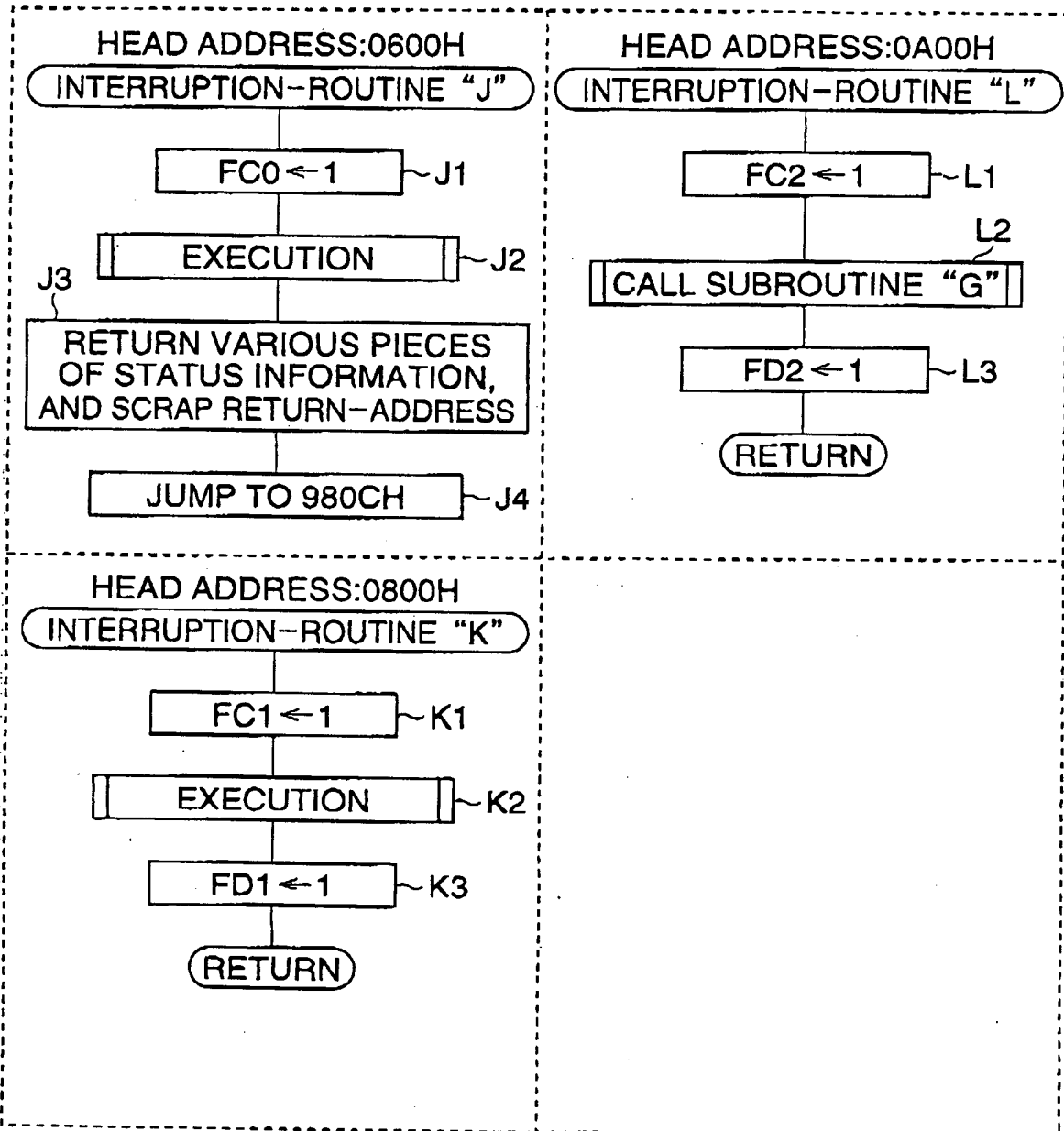
FIG. 30



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FIG. 31

PARTIAL AREA OF RAM 14



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FIG. 32

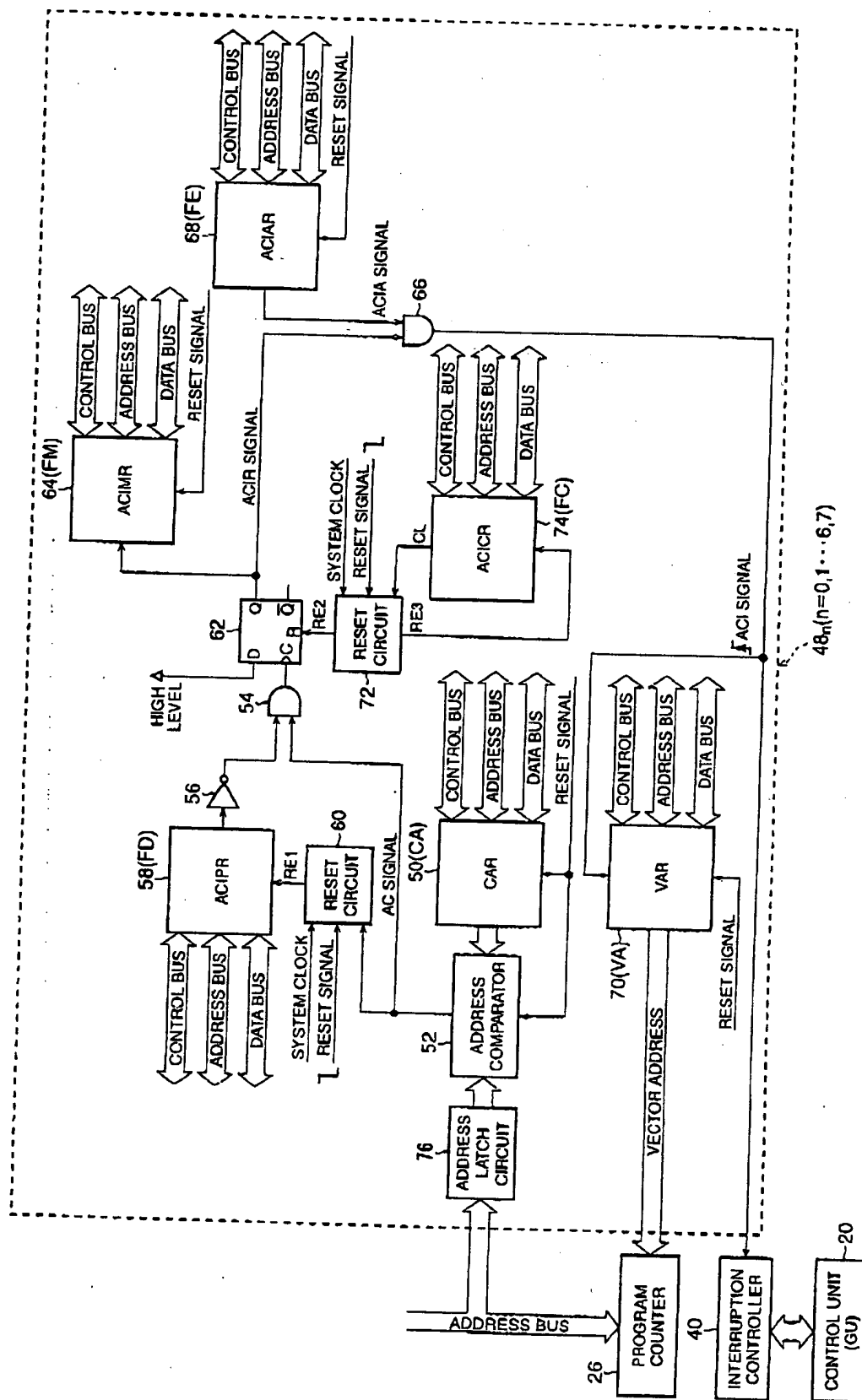
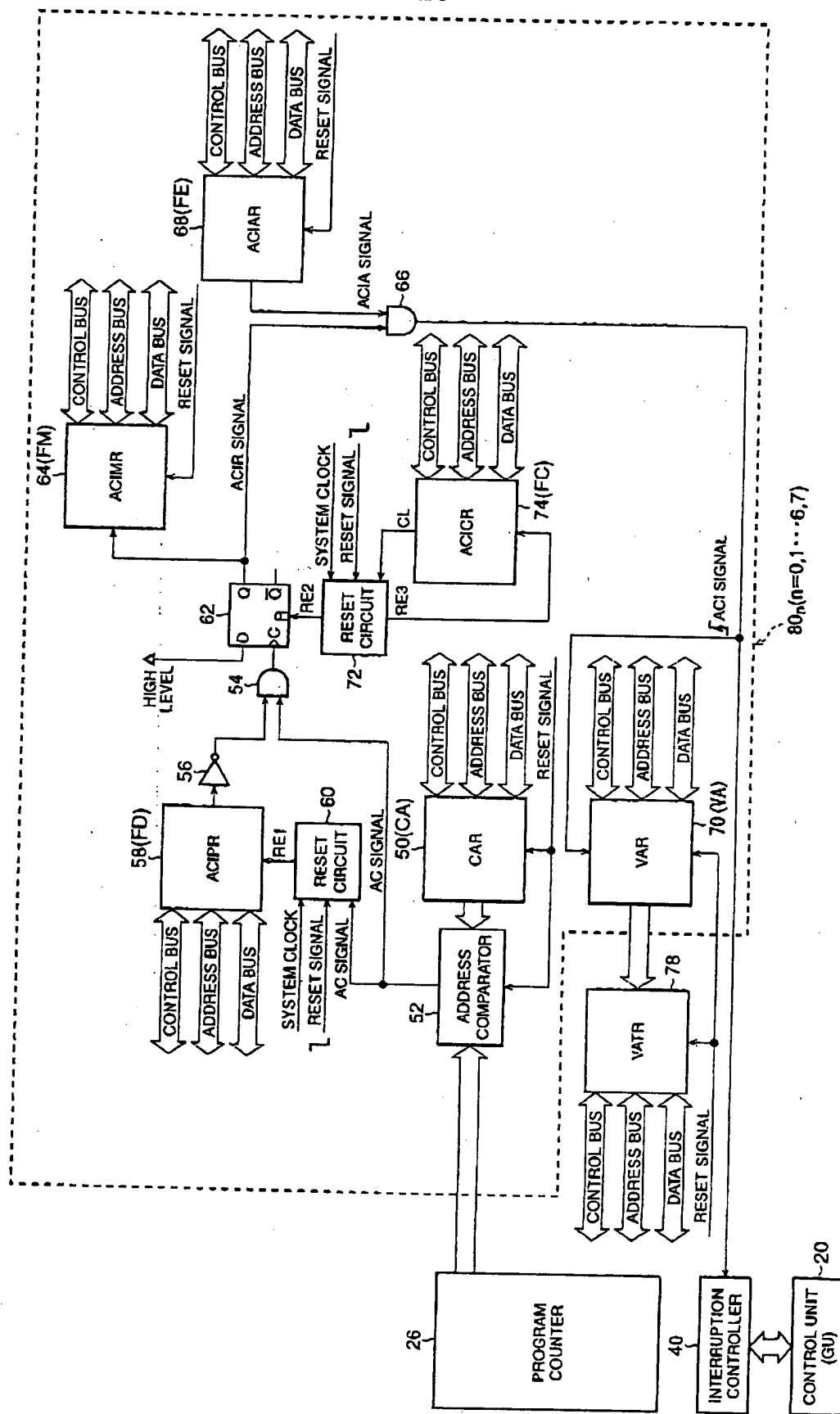
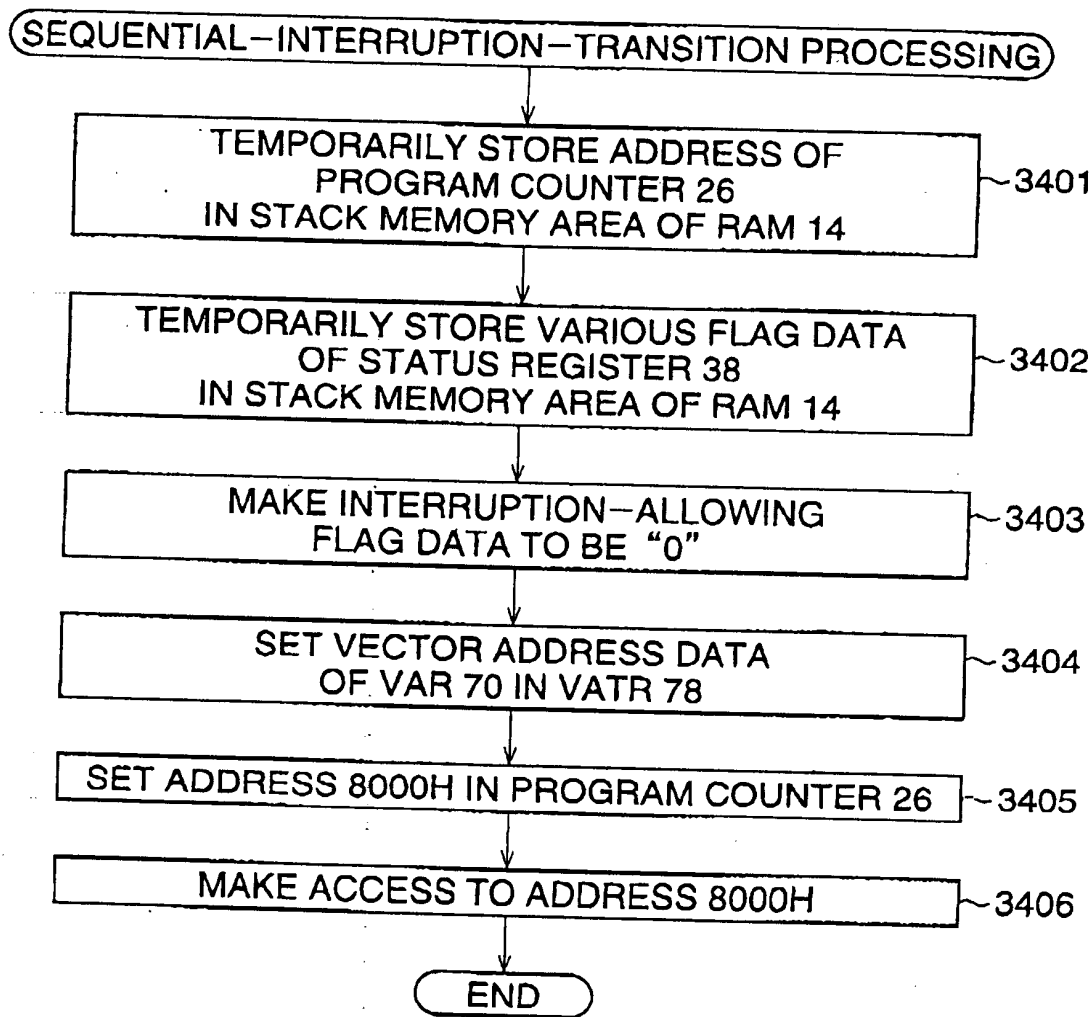


FIG. 33



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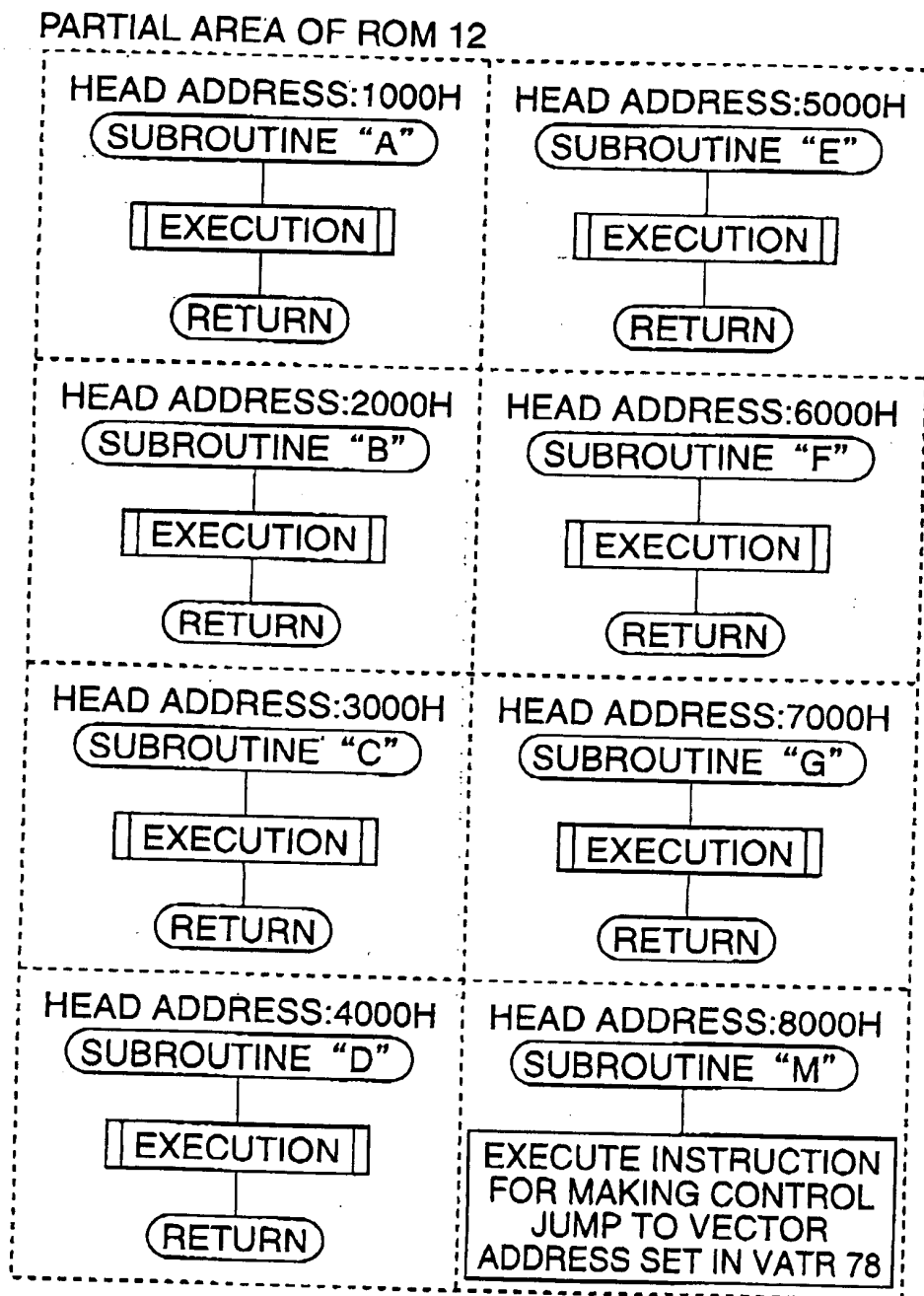
FIG. 34



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FIG. 35



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FIG. 36

